Huawei International Pte. Ltd. sells and markets telecommunication network products and solutions. The Company offers application, software, storage, network security, data communication, energy, and infrastructure products.

The System Architecture team of Huawei Singapore Research Centre is seeking a highly motivated individual responsible for circuits design for large-scale/low-power/high-performance ASIC chips.

**Position Title: Senior Circuit Design Engineer**

**Duties & Responsibilities**

- Design and implement custom digital circuits such as DFFs and MUXes.
- Build schematic and co-work with layout engineer for cell or macro delivery.
- Perform timing/power simulation and analysis.
- Perform reliability checks, i.e. IR, EM, Aging.
- Characterize custom cells.
- Make macro-level floor-plan to optimize area.

**Skills/Experience**

- Ph.D degree in EE or MS with 3+ years of industry experiences in the area of high-performance circuit design
- Circuit/Layout understanding with deep submicron technologies
- In-depth understanding of factors affecting circuit layout, performance, and power
- Hands on experience on standard cell or SRAM design is plus
- Familiarity with standard cells design/characterization/verification and timing files
- Expertise in using EDA tools such as virtuo, hspice, Spectre, Liberate, Primetime.
- Scripting proficiency such as perl, python, tcl.

Please email your CV to zhou.weijing@huawei.com by 30 October 2020.