**FOIT – Foxconn Optical Interconnect Technology, Inc.**

Foxconn Optical Interconnect Technology ("FOIT") is a business unit under Foxconn Interconnect Technology (FIT) responsible for commercial development and production of high speed multi-mode and single mode fiber optic transceivers and solutions.

_Headquartered in San Jose, CA, the FOIT organization has development teams in Singapore and USA (San Jose CA, Breinigsville, PA)._

FIT is a global leader in the supply of precision components. FIT is a public company since 2017, listed on the Hong Kong Stock Exchange as FIT Hon Teng HKG:6088.

[https://www.foit-foxconn.com/company](https://www.foit-foxconn.com/company)

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**Principal QA Engineer**

**Key Job Responsibilities:** (point 4-6 are highly recommended)

1. Core knowledge and skill-set on product quality
2. Early involvement at NPI stage through to PRR
3. Engage in continuous improvement of product quality
4. Effective execution of NPI Quality
5. Ensure CM readiness (hardware, software and resources) prior to Beta qualification
6. Define STRIFE and Beta qualification plan and execute per defined milestone
7. FMEA and past lessons learned to be leveraged upon prior to Beta Sample built. Establish PMP.
8. Ensure Regulatory/Environmental, L1/L2 tests, EPCDS, GDA completed together with other NPI Trackers' deliverables. Report submitted per commitment to first
9. Product Quality & Reliability
10. Gap analysis of field and reliability failures to drive continuous improvement of product quality
11. Define qualification plan for component second source and cost reduction initiatives and ensuring no deterioration of product quality
12. Test plan buy-off
13. Customer Interface: take the lead in engaging OEM customers on product quality related issues

**Key Job Requirements** (point 4-5 are highly recommended):

1. Bachelor In Microelectronics/Electronics/Communications Engineer
2. Possess excellent Team work, communication skills and able to converse in Mandarin (to liaise with Mandarin speaking associates)
3. Possess strong leadership, EQ and good customer
4. Knowledge and experience in fiber optics transceiver new product qualification and execution is a plus
Experience in NPI qualification of optical transceiver modules, generating and execution of qualification plan

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**R&D Test Development Engineer**

**Key Responsibilities (point 3-5 are highly recommended)**

15. Be part of our growing Test Engineering team chartered to provide R&D test platform development for fiber optics transceiver testing.
16. Learn about advance fiber optics transceiver product test knowledge and at the same time, drive initiatives on continuous test improvement to enhance testers' effectiveness and efficiency. These constitute intelligent test time reduction, developing new test methodologies, platforms, and establishing cutting edge hardware configuration.
17. Work alongside with US R&D counterparts on test development for new generation products.
18. Assist and manage test platform development and responsibilities to ensure prompt release of R&D testers to production lines.
19. Generate and maintain up-to-date test documentation

**Key Job Requirements:**

1. Degree in Electrical/Electronics Engineering with at least 5 years’ related experience in manufacturing testing and an understanding of fiber optics technology trends
2. Functional knowledge of KeySight/JDSU fiber optics test equipment will be advantageous, whilst good analytical and troubleshooting skills are essential
3. Ability to speak and write simple Chinese to liaise with China’s contract manufacturers will be advantageous.
4. Creativity and proficiency in software programming is required, and knowledge of NI Labview, TestStand, .NET VC++ and SQL will be advantageous
5. Self-motivated team player and able to work with minimum supervision.
6. Required to travel overseas on business needs.
7. Candidate without prior experience will be considered. Candidate with prior experience will be considered for senior role.

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**Hardware Development Engineer**

**Key Job Responsibilities**

We are looking for an enthusiastic and passionate Electrical Engineer with background in Hardware Design and willing to learn new design skills in the growing market of Optical Communications arena. He or she, will:

1. Be part of a worldwide R&D group, supporting the development of transceivers.
2. Design the Hardware part of Optoelectronic Transceivers and Active Optic Cable (AOC) operating at bitrate of 100Gb/s and above
3. Work hands on with SW developers, Mechanical engineers, Test Engineers to characterize, debug and improve the product performance
4. Generate detailed Engineering Requirements Specifications (ERS) for SW designers and Test Engineers
5. Be exposed to most advanced test Techniques and Equipment for high speed Optical communications like Digital Sampling Scopes, real time scopes, Bit Error Rate Testers to capture HW and SW design issues
6. Generate documentation like Data Sheet and Application Notes
7. Optical Devices (e.g. Semiconductor lasers and Photo receivers) knowledge is a plus
8. Be able to design Printed Circuit Boards Layout, will be a plus
Key Requirements
1. Bachelor, Master or PhD in Engineering
2. Passion for Electronics, Optoelectronics and/or Optical Communications
3. Highly innovative mind-set to generate new ideas and approaches in overcoming complex issues
4. Willing to learn and accept technical challenges
5. Excellent communication skills, both verbal and written
6. Maintain high degree of integrity and discretion

Extra Requirements (good to have):
7. Knowledge of HFSS, Keysight EMPro and Keysight ADS is a plus
8. Previous experience with Optoelectronic Transceivers design, is a plus
9. Be able to travel at least 10% of the time (on average)

Send your resumes indicating the Job Title to:

ken-kh.lim@foit-foxconn.com