We Are OmniVision

A leading developer of advanced digital imaging solutions.

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Digital IC Design Verification Engineer (Fresh Graduates)

Omnivision Technologies is a leading developer of advanced digital imaging solutions, with best-in-class CMOS image sensor technology. In the Singapore ASIC Design Centre, you would be part of an experienced and talented design team to develop Video Processor SoC, with advanced video analytics for smart home, security, automotive & IoT applications. From innovative video processor architectures, to advanced video analytics, you can have a hand in breakthroughs and reinventing the digital world.
Job Description

As design verification engineer, you would be part of a passionate verification team that is constantly pushing the limits – developing and deploying state-of-the-art verification methodologies in ever-increasing design complexities, from UVM, C/C++ co-simulation, system emulation to mixed-mode simulation & formal verification. The goal is simple – to achieve zero-defect with the best and smartest approach to the large verification space.

Requirements:

1. Disciplined, quality-minded, and highly driven for excellence,
2. Excellent team player and good communication skills
3. MSEE/BSEE in Electrical Engineering or Computer Engineering
4. Passionate and strong in general programming is a plus

Staff/Senior Digital IC Design Engineer

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Job Description

You would be part of a dynamic digital IC design team that develops state-of-the art video processing, advanced video analytics accelerators, and full Video Processor SoC. You would be joining the team to explore, evaluate and define innovative design micro-architectures, to meet the technological requirements of the IP and SoC. You would be responsible for the successful implementation of the designs in Verilog/SystemVerilog coding, metrics measurement to meet key objectives & results, and follow-through in comprehensive verifications to successful silicon.

Requirements:

1. Experience in micro-architectures design, and RTL coding with Verilog/SystemVerilog
2. Experience in synthesis, with analysis of power, timing and area
3. Disciplined, quality-minded, and highly driven for excellence
4. Excellent team player and good communication skills
5. MSEE/BSEE in Electrical Engineering or Computer Engineering
6. Candidates with relevant experiences would be offered as Senior or Staff, taking on higher responsibilities
7. Experience in video processing and video analytics is a plus
8. Experience in UVM verification methodology is a plus
9. Passionate and strong in general programming is a plus

Send your resumes to: chintong.thia@ovt.com